

## SYSTEM OUTLINE

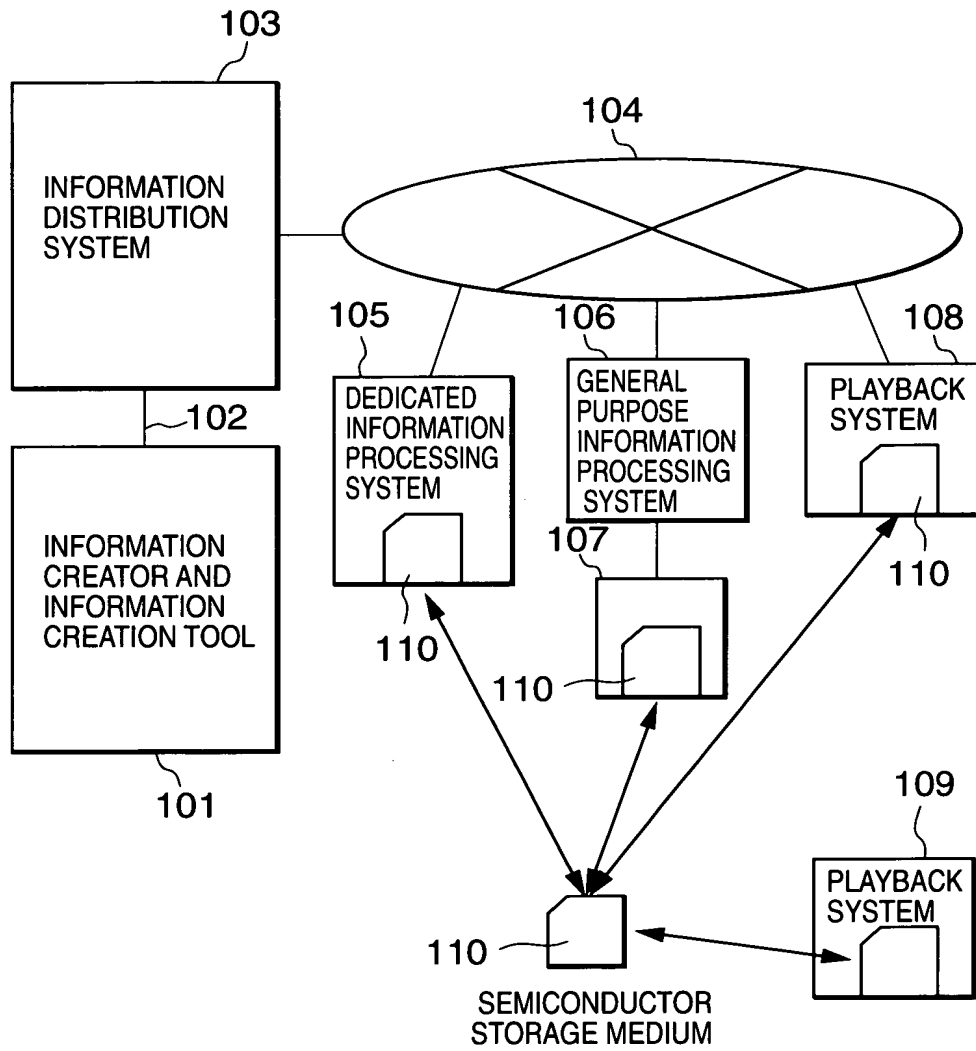


FIG. 2

## FLASH MEMORY CARD

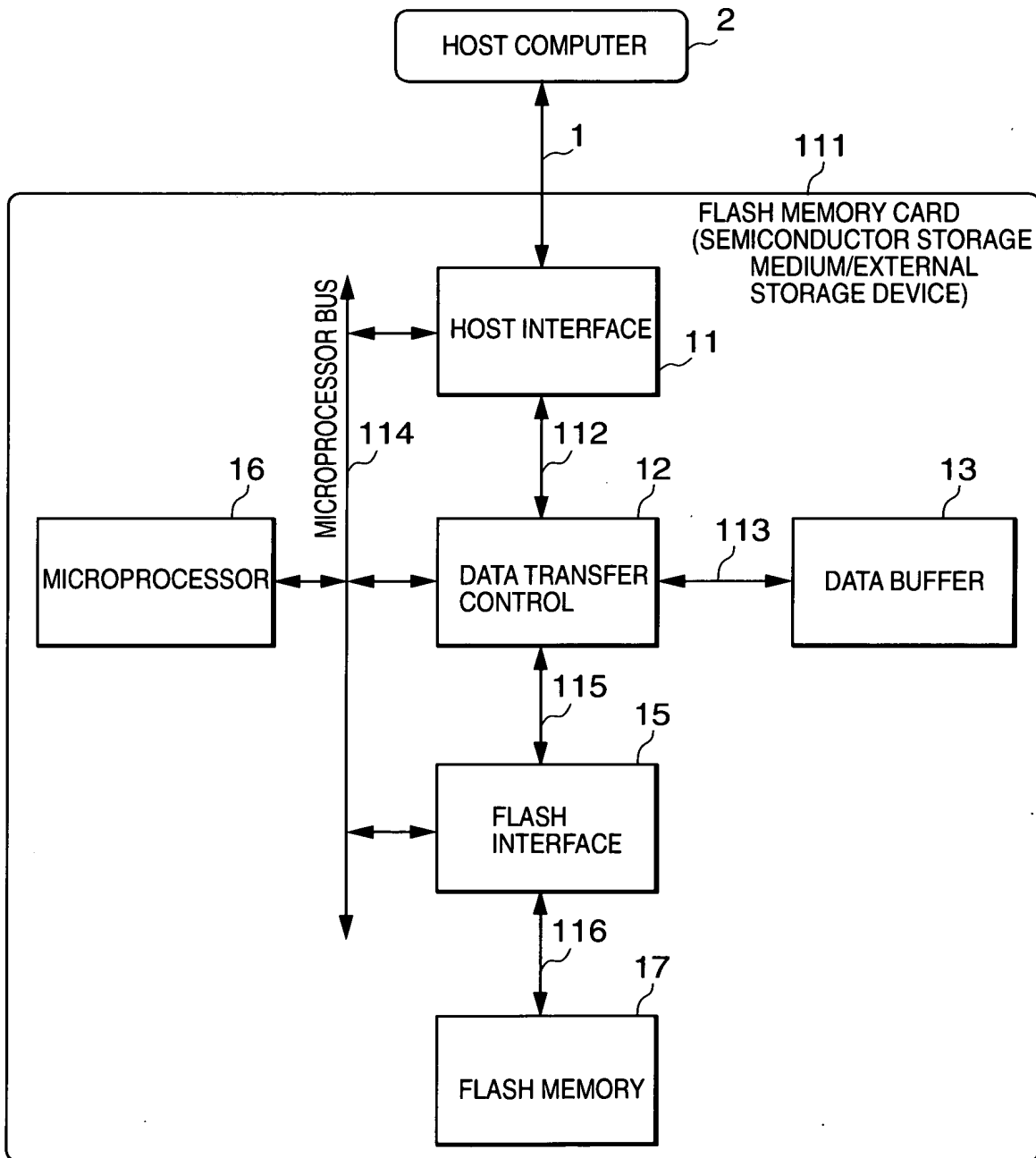
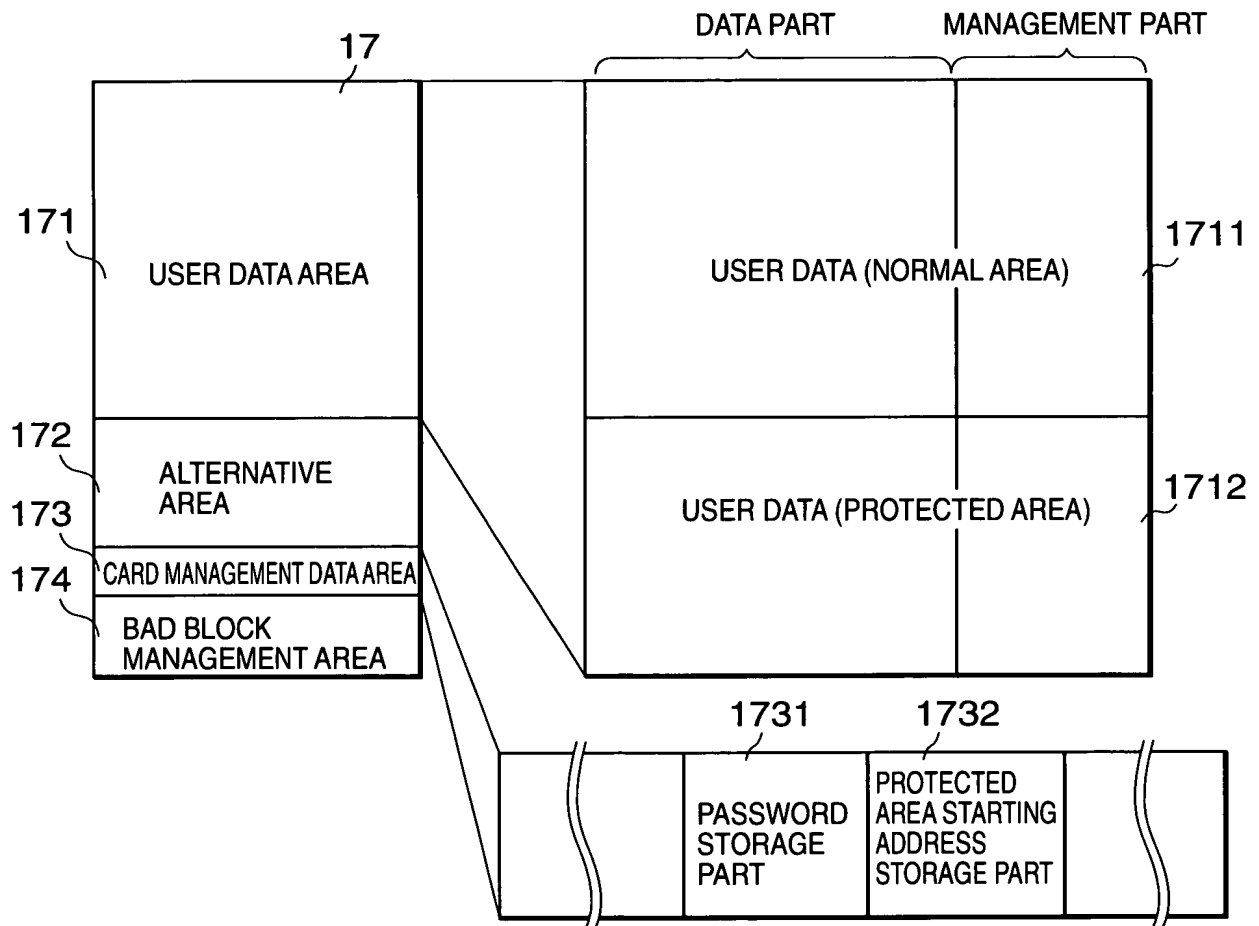
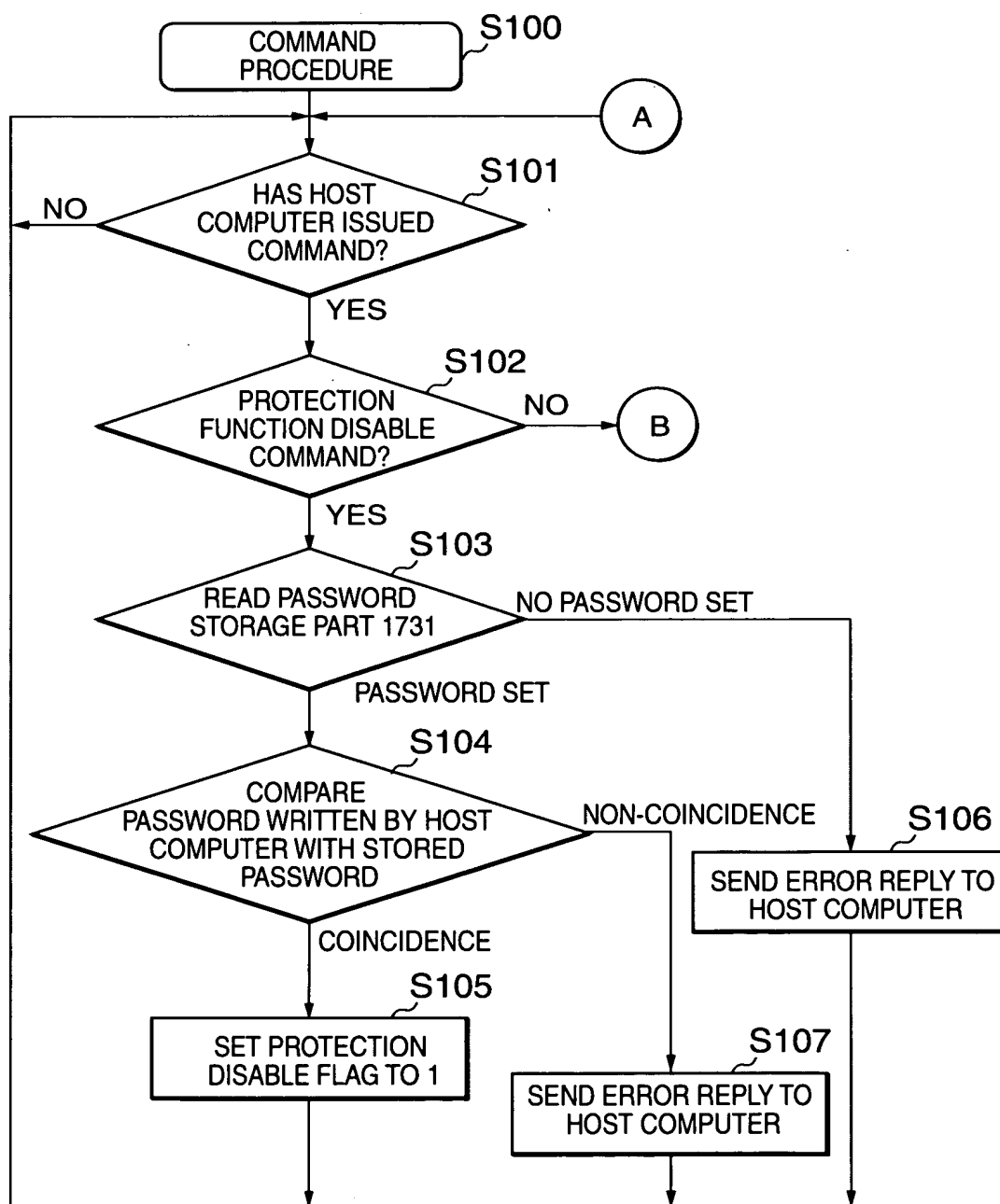


FIG. 3

## CONSTRUCTION OF FLASH MEMORY

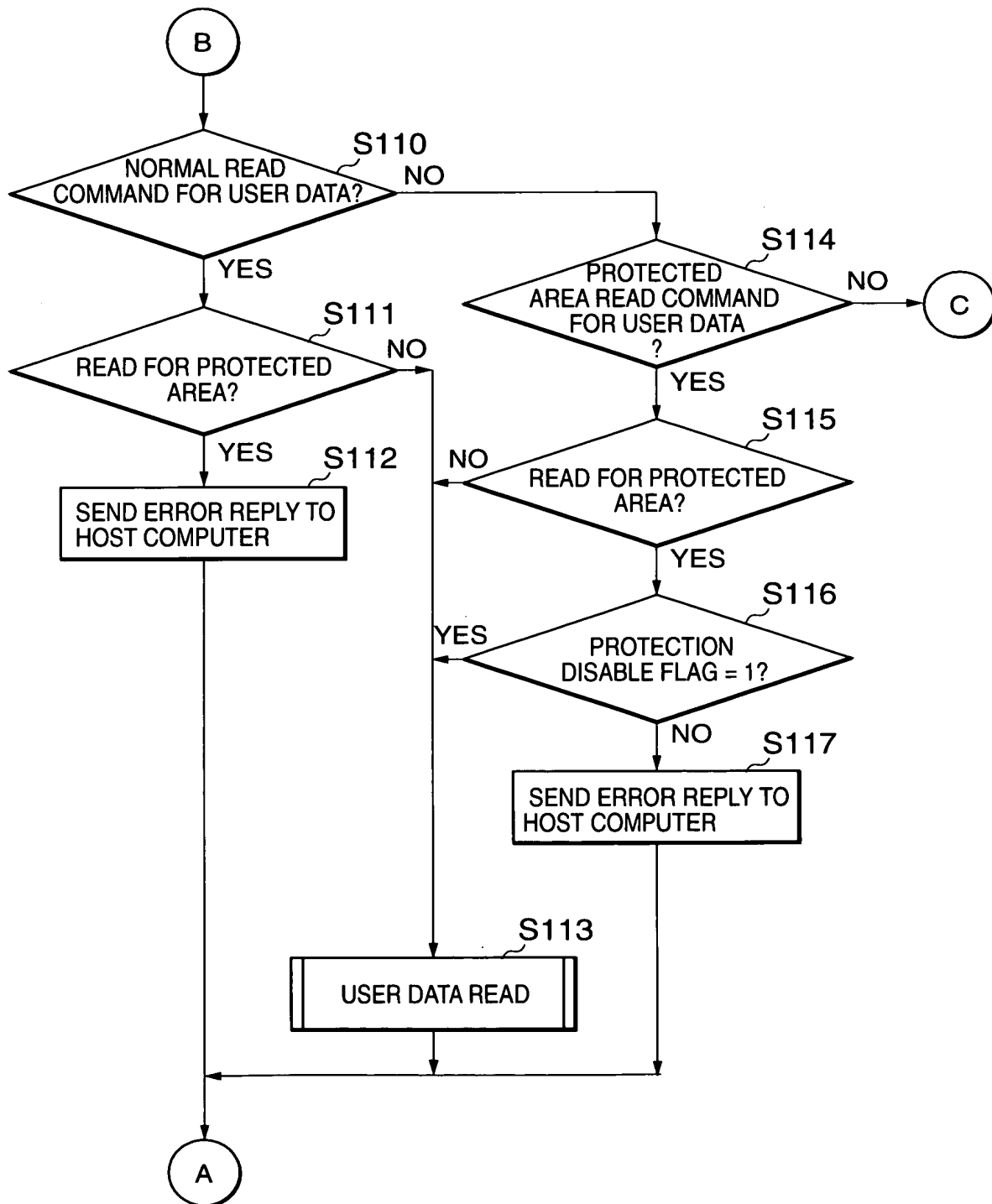


## COMMAND PROCEDURE

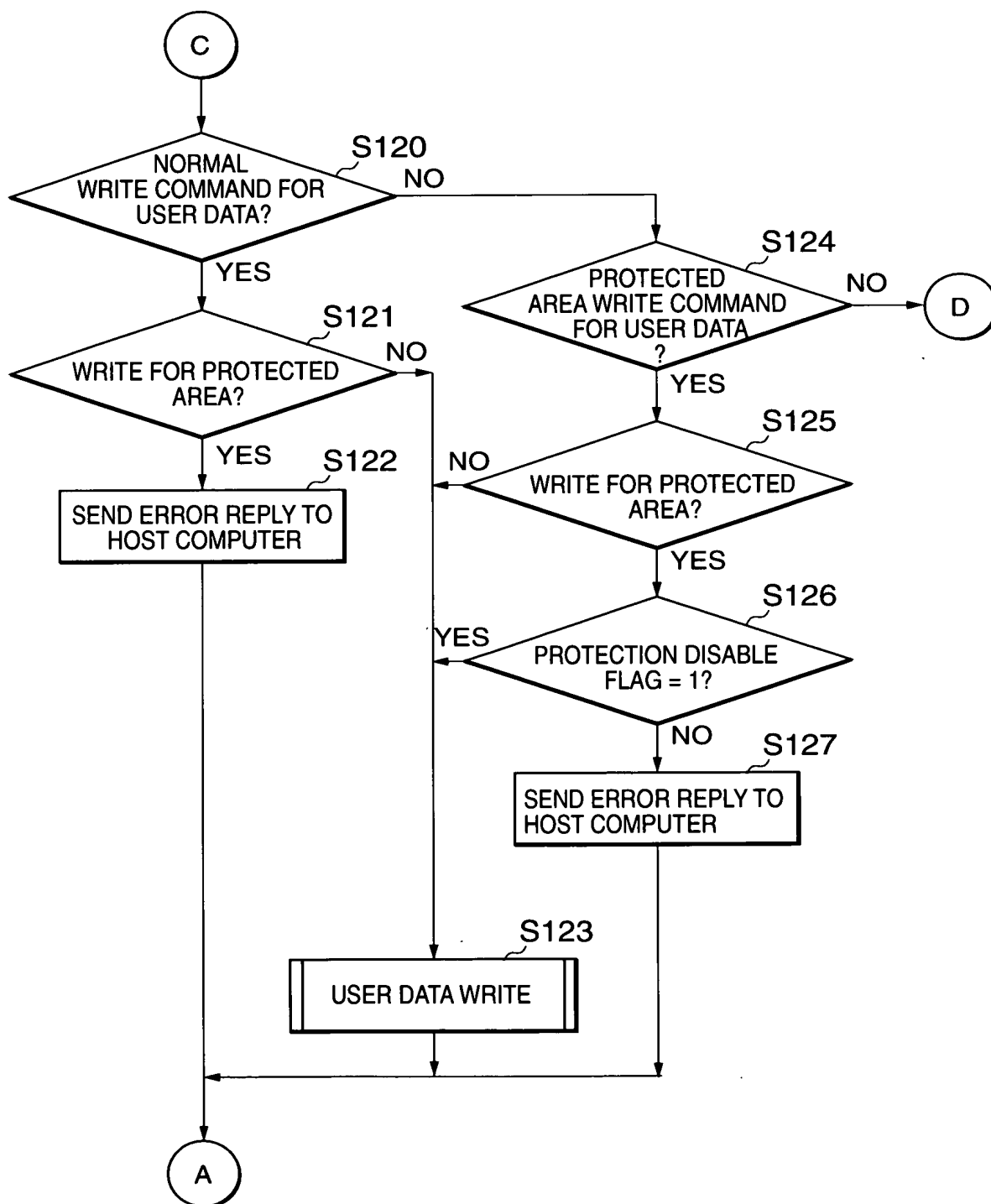


## FIG. 5

COMMAND PROCEDURE (CONTINUED FROM FIG. 4)

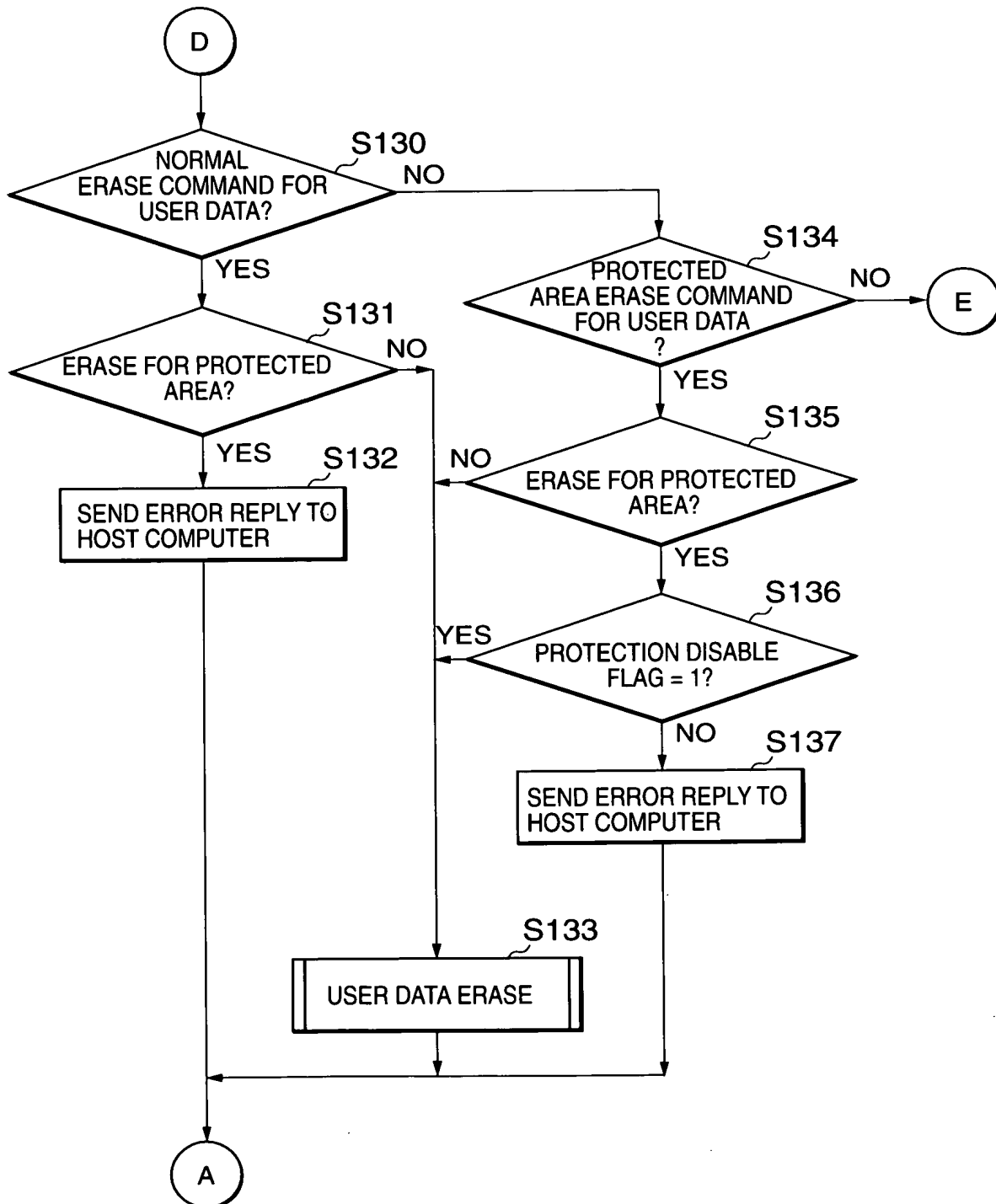


### COMMAND PROCESSING (CONTINUED FROM FIG. 5)



## FIG. 7

COMMAND PROCESSING (CONTINUED FROM FIG. 6)



## FIG. 8

COMMAND PROCESSING (CONTINUED FROM FIG. 7)

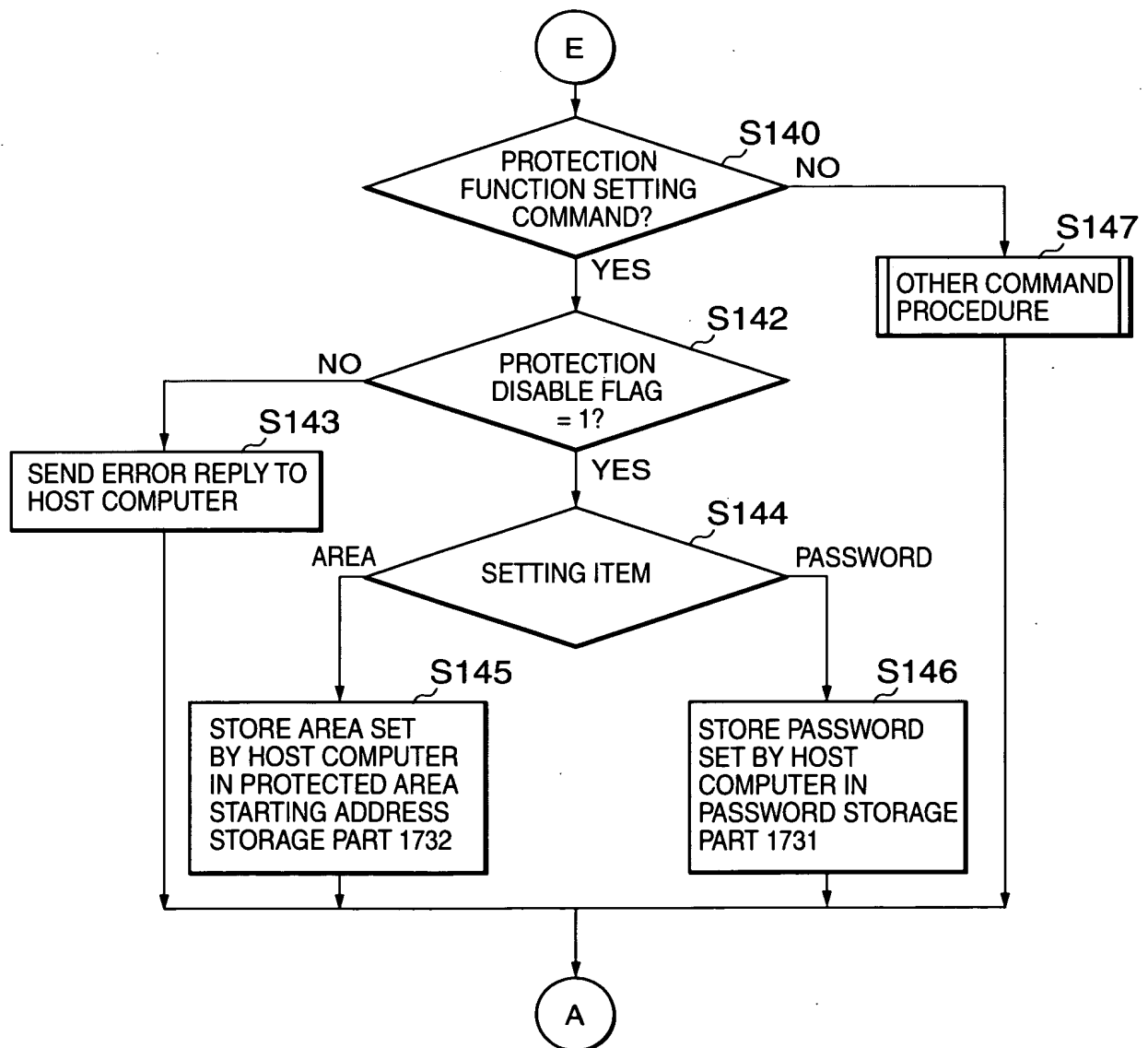
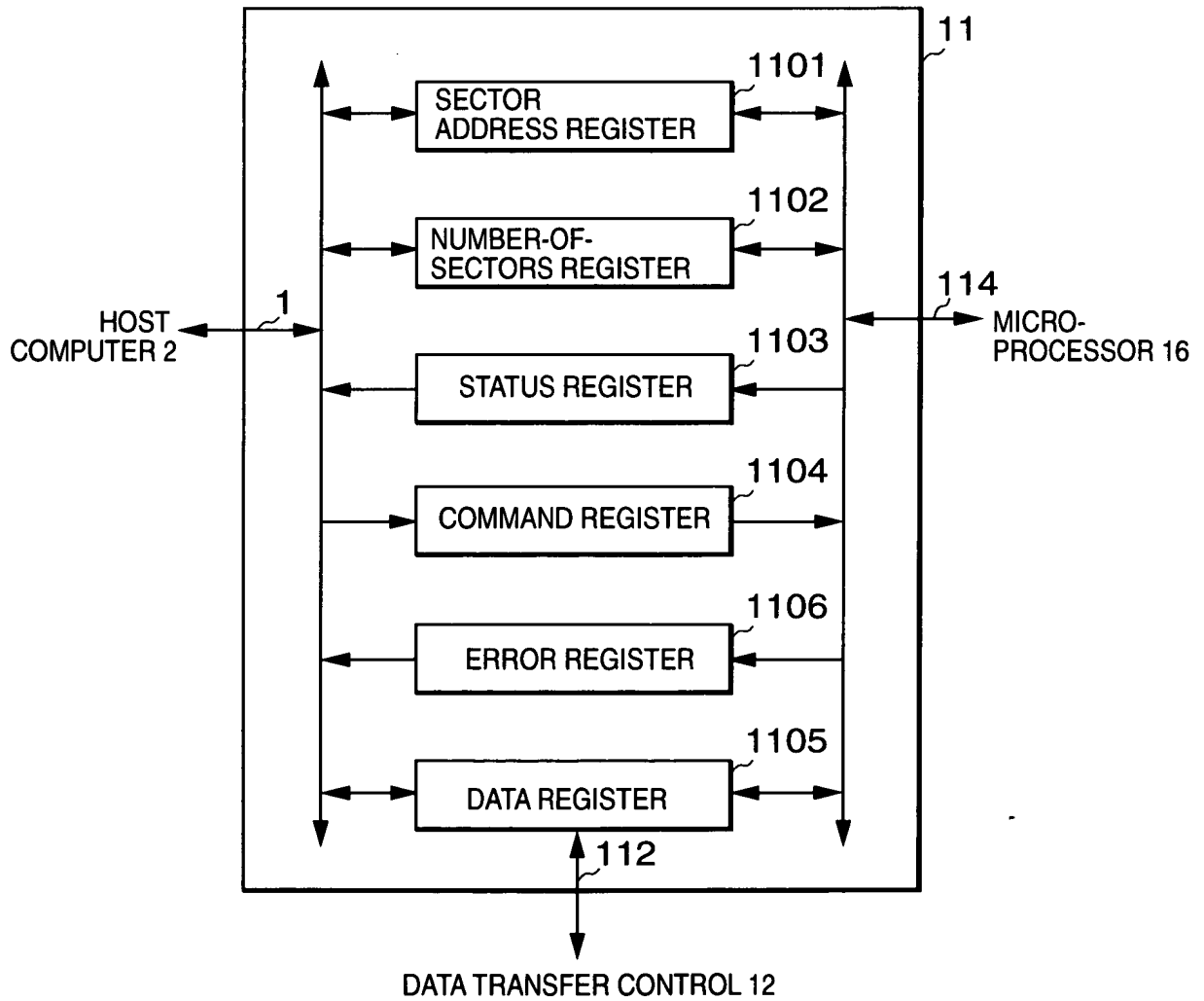




FIG. 9

HOST INTERFACE





# FIG. 11

## POWER-ON PROCEDURE IN ANOTHER EMBODIMENT

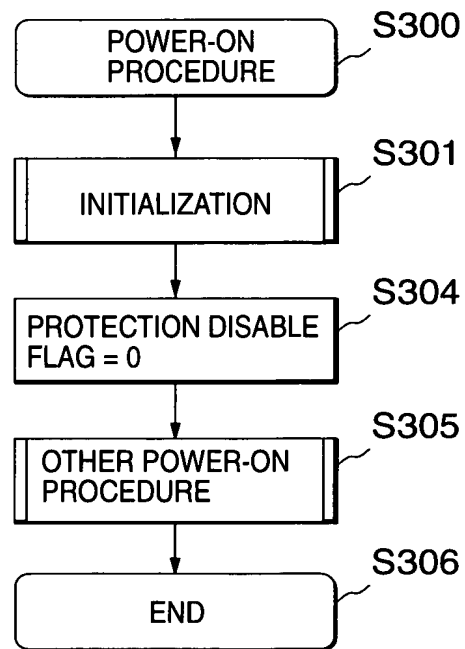




FIG. 13

## POWER-ON PROCEDURE IN ANOTHER EMBODIMENT

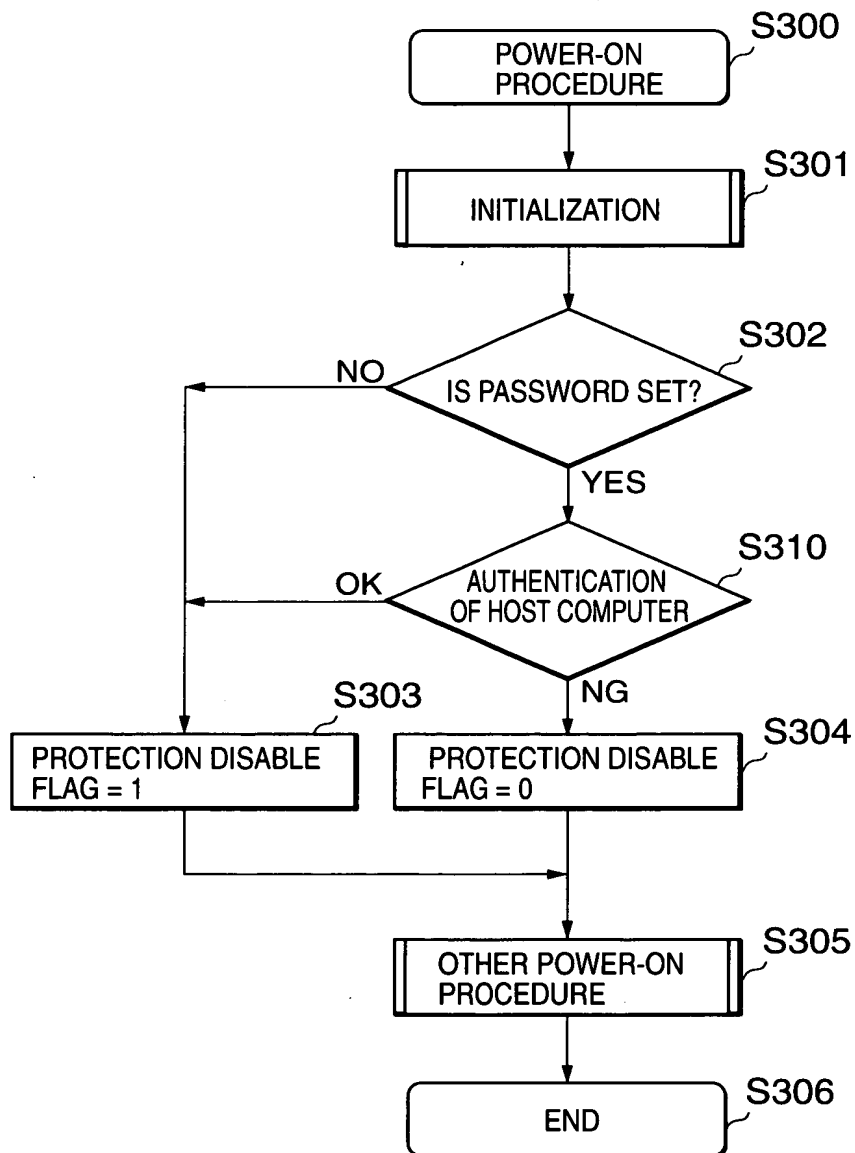


FIG. 14

## READ COMMAND PROCEDURE IN ANOTHER EMBODIMENT

